### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Kei MURAYAMA

Serial Number: Not Yet Assigned

Filed: January 5, 2004

For: SEMICONDUCTOR DEVICE MANUFACTURING METHOD

### INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

January 5, 2004

Sir:

In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (4)

	INFORMATION DISCLOSURE STATEMENT	Atty. Docket No. 031353	Serial No. New Application		
<b>'</b>		Applicant(s): Kei MURAYAMA			
	PTO-1449	Filing Date: January 5, 2004	Group Art Unit: Not Yet Assigned		

# **U.S. PATENT DOCUMENTS**

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA						
	AB						:
	AC						
	AD						

# FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
 AE	59-222954	12/14/84	Japan	Yes-Abstract/Discussed in the specification
 AF	63-156348	06/29/88	Japan	Yes-Abstract/Discussed in the specification
 AG	61-88546	05/06/86	Japan	Yes-Abstract/Discussed in the specification
 AH	2000-286304	10/13/00	Japan	Yes-Abstract/Discussed in the specification
 AI				

# **OTHER DOCUMENTS**

	AJ		
	AK		
Examiner		Date Considered	